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EUROPEAN PATENT APPLICATION

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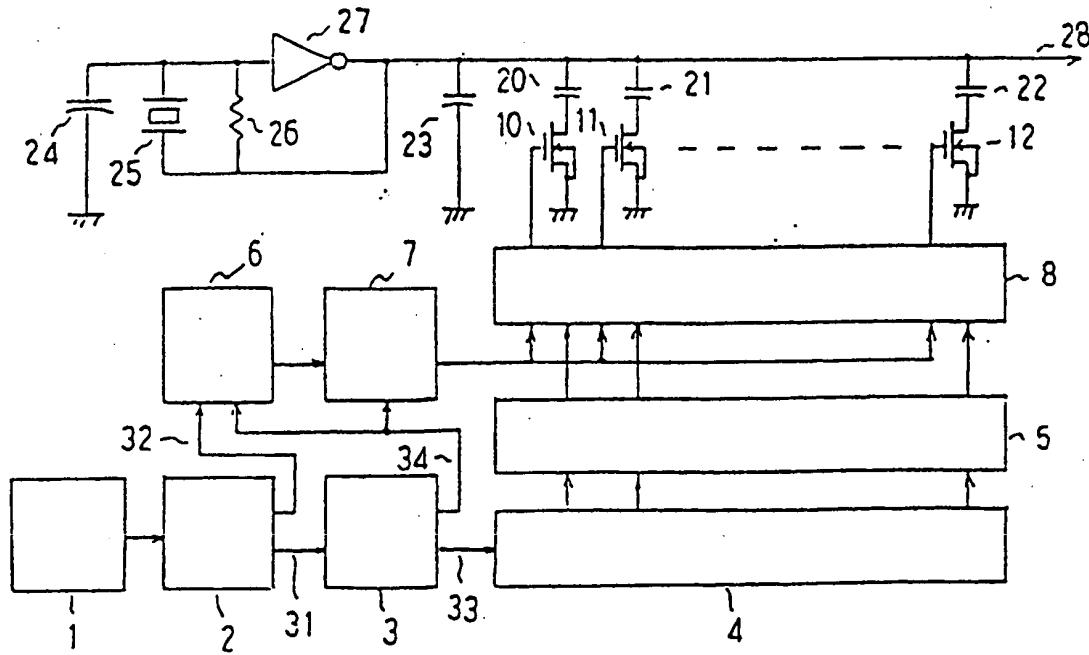
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㉓ Variable capacitance capacitor array.

㉔ A capacitor array provides a variable capacitance, having FET's as respective switching elements (10, 11, 12) for switching each capacitor (20, 21, 22) in the array on and off, and means (8) for controlling switching of the switching elements to switch selected ones of the capacitors on, characterised in that the control means are arranged to co-operate with voltage generation means (7) so as to supply to a control electrode of each switching element for switching on a selected said capacitor a voltage which causes the impedance provided by said switching element to change between multiple levels.



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Fig. 7



EP 90 31 3141

| DOCUMENTS CONSIDERED TO BE RELEVANT | | | |
|---|---|--|---|
| Category | Citation of document with indication, where appropriate, of relevant passages | Relevant to claim | CLASSIFICATION OF THE APPLICATION (Int. Cl.5) |
| A | PATENT ABSTRACTS OF JAPAN vol. 11, no. 277 (E-538)(2724) September 8, 1987 & JP-A-62 76 801 (NEC CORP) April 4, 1987 * the whole document * | 1,5,7,8 | H03L1/02 |
| A | PATENT ABSTRACTS OF JAPAN vol. 11, no. 292 (E-543)(2739) September 19, 1987 & JP-A-62 91 006 (MATSHIMA KOGYO CO LTD) April 25, 1987 * the whole document * | 1,5,7,8 | |
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| A | EP-A-133 388 (THOMSON-CSF) * page 2, line 28 - page 4, line 21; figure 1 * | 1,5,7,8 | |
| A | PROCEEDINGS OF THE IEEE 1989 CUSTOM INTEGRATED CIRCUITS CONFERENCE, MAY 15-18, PAGES 1611-1615, NEW YORK, U.S.A. ROESGEN AND WARREN: "AN ANALOG FRONT END CHIP FOR V.32 MODEMS". | | HO3L HO3H HO3B |
| <p>The present search report has been drawn up for all claims</p> | | | |
| Place of search | Date of completion of the search | Examiner | |
| THE HAGUE | 14 AUGUST 1991 | PEETERS M. M. G. | |
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